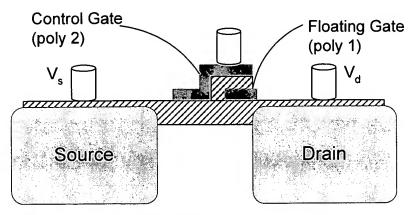
Patent Application For: SINGLE-POLY 2-TRANSISTOR BASED FUSE ELEMENT Inventor: Chandrasekharan Kothandaraman, et al.

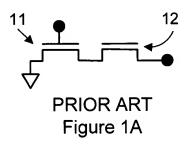
Reference No. 2002 P 03440 US Page 1 of 7

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PRIOR ART Figure 1



SG EE	Erase	Program	Read
SG	-17 V	Vt	Vref
Wordline	(-12 V)		
(WL)			
Bitline	Vss	12 V	Vdd
(BL)	(5V)		
Source	Vss	Vss	Vss
Line (SR)			

PRIOR ART Figure 1B